Electronic Pate	ent App	olication Fe	e Transm	nittal					
Application Number:	10	10707205							
Filing Date:	26	26-Nov-2003							
Title of Invention:		SYSTEM AND METHOD TO TEST INTEGRATED CIRCUITS ON A WAFER							
First Named Inventor/Applicant Name:	La	Lawrence Pileggi							
Filer:	CI	Charles L. Moore/Katie Efland							
Attorney Docket Number:	36	361007.000035							
Filed as Large Entity									
Utility Filing Fees									
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)				
Basic Filing:									
Pages:									
Claims:									
Miscellaneous-Filing:									
Petition:									
Patent-Appeals-and-Interference:									
Post-Allowance-and-Post-Issuance:									
Extension-of-Time:									

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)	
fiscellaneous:					
Request for continued examination	1801	1	790	790	
	Tota	Total in USD (\$)			